

Notice of Allowability	Application No.	Applicant(s)	
	09/649,437	REYNOLDS ET AL.	
	Examiner	Art Unit	
	Eduardo Garcia-Otero	2123	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Request for Continued Examination received 11/01/2004.
2. ☒ The allowed claim(s) is/are 1-29.
3. ☒ The drawings filed on 01 November 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|---|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

ALLOWANCE

Introduction

1. Title is: METHOD AND APPARATUS FOR SPECIFYING ADDRESSABILITY AND BUS CONNECTIONS IN A LOGIC DESIGN
2. First named inventor is: REYNOLDS
3. Applicant's Request for Continued Examination was received 11/01/04, including amended claims and formal drawings.
4. Claims 1-29 are pending, and are allowed.
5. US application filing date (corrected) is 8/23/04, and no earlier priority is claimed.

Index of Important Prior Art

6. Tabak refers to Advanced Microprocessors, by Daniel Tabak, McGraw-Hill, Inc., ISBN: 0-07-062843-2, 1995.
7. Microsoft Computer Dictionary refers to Microsoft Computer Dictionary, Fourth Edition, by Microsoft Press, JoAnne Woodcock as Senior Contributor, ISBN 0-7356-0615-3, May 1999.
8. McGraw-Hill Dictionary refers to The McGraw-Hill Dictionary of Scientific and Technical Terms, Sixth Edition, by McGraw-Hill Companies, Inc., ISBN 0-07-042313-X, 2003.
9. Dangelo'678 refers to US Patent 6,324,678.
10. Hennessy refers to Computer Architecture A Quantitative Approach, by John L. Hennessy et al, Second Edition, Morgan Kaufmann, 1996, ISBN 1-55860-329-8, pages 73-74.
11. Kerstein refers to US Patent 6,393,548.
12. Arroyo refers to US Patent 5,446,845.
13. Story refers to US Patent 6,434,654.

Applicant Remarks

14. At Remarks page 9, Applicant that the prior art of record does not disclose "lane matching". However, Tabak's microprocessors inherently match lanes because the databus sizes do not match. See Tabak page 4 Table 1.1, specifically the Intel 8088, 80188, and 30376 microprocessors.
15. For the record, the Examiner is providing additional prior art below that more explicitly discloses lane matching.
16. However, all pending claims are allowable for the reasons stated below.

REASONS FOR ALLOWANCE

17. Claim 1 (currently amended) is allowable because it is not obvious to combine the limitation “using a logic design component to specify addressability...” with the limitation “replacing the logic design component...”. There is no motivation in the prior art to combine these limitations. Claims 2-17 are allowable for the same reason as claim 1.
18. Claim 18 (currently amended) is allowable because it is not obvious to combine the limitation “selecting a logic component...” with the limitation “instantiating a logic...”. There is no motivation in the prior art to combine these limitations. Claims 19-29 are allowable for the same reason as claim 18.
19. Thus, all pending claims (1-29) are allowed.
20. All prior rejections and objections are withdrawn.

Additional Prior Art

21. Kerstein US Patent 6,393,548 column 11 line 23 discloses “signal... define the byte lane in the register where valid data is transferred”.
22. Arroyo US Patent 5,446,845 Abstract discloses “Data bus steering logic routs data between various byte lanes of the system bus”.
23. Story US Patent 6,434,654 Abstract discloses “serial data bus that has a variable width for the transfer of data between devices”.
24. Hennessy page 73 discloses “Little Endian byte order... Big Endian Byte order... byte order is a problem when exchanging data among machines with different orderings”.

Conclusion

25. All pending claims 1-29 are allowed.

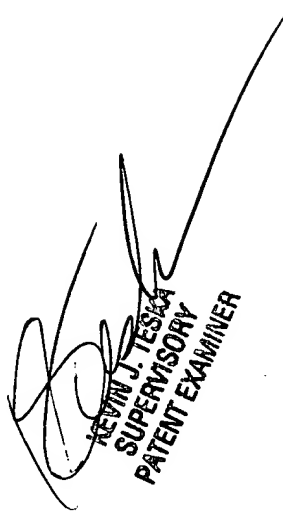
Communication

26. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eduardo Garcia-Otero whose telephone number is 703-305-0857. The examiner can normally be reached on Tuesday through Friday from 9:00 AM to 8:00 PM. If attempts to reach the Examiner by telephone are unsuccessful, the Examiner’s supervisor, Kevin Teska, can be reached at (703) 305-9704. The fax phone number for this group is 703-872-9306. Any inquiry of a general nature or relating to the status of this application or

Art Unit: 2123

proceeding should be directed to the group receptionist, whose telephone number is (703) 305-3900.

* * * *



KEVIN J. TESLA
SUPERVISORY
PATENT EXAMINER